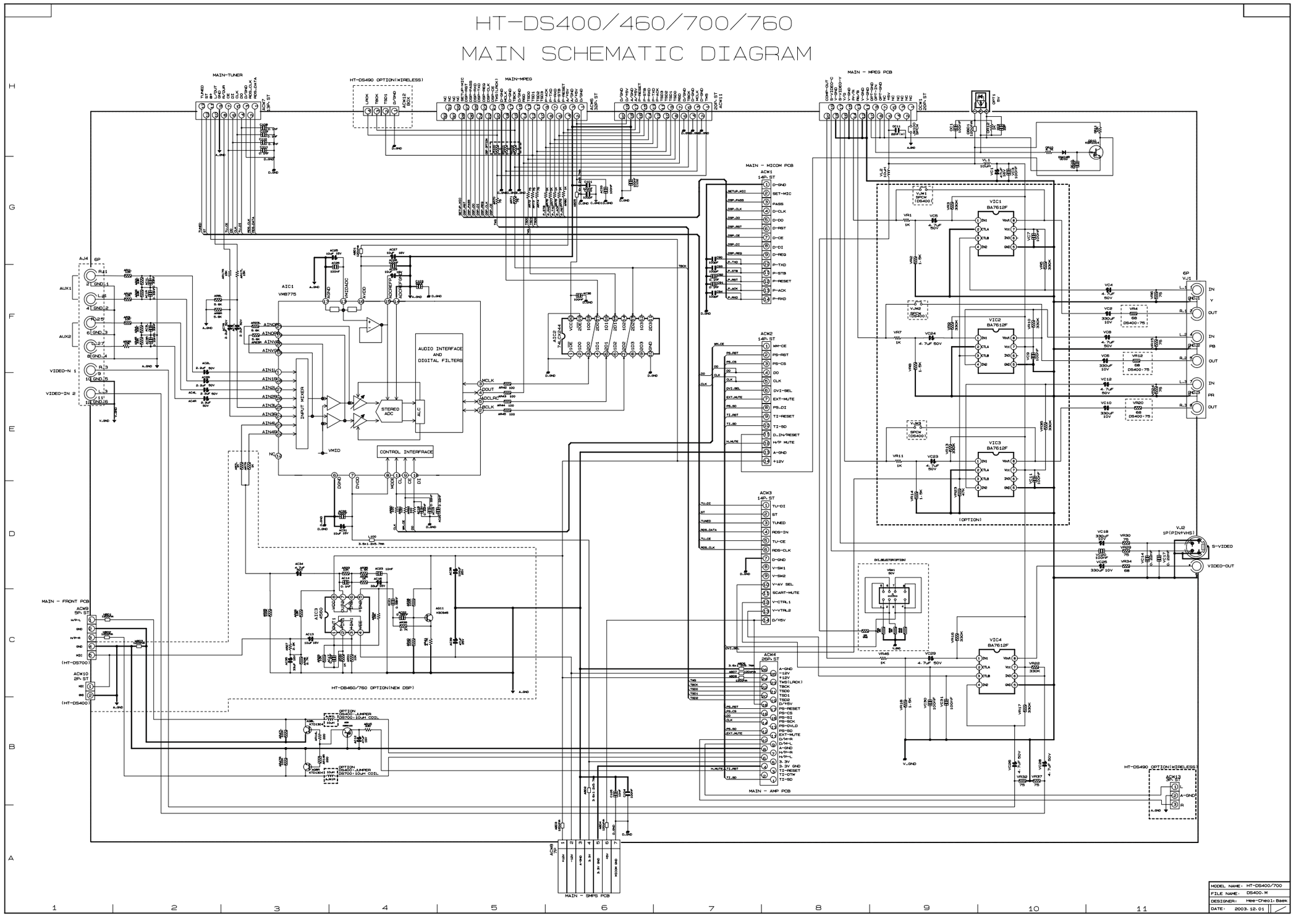
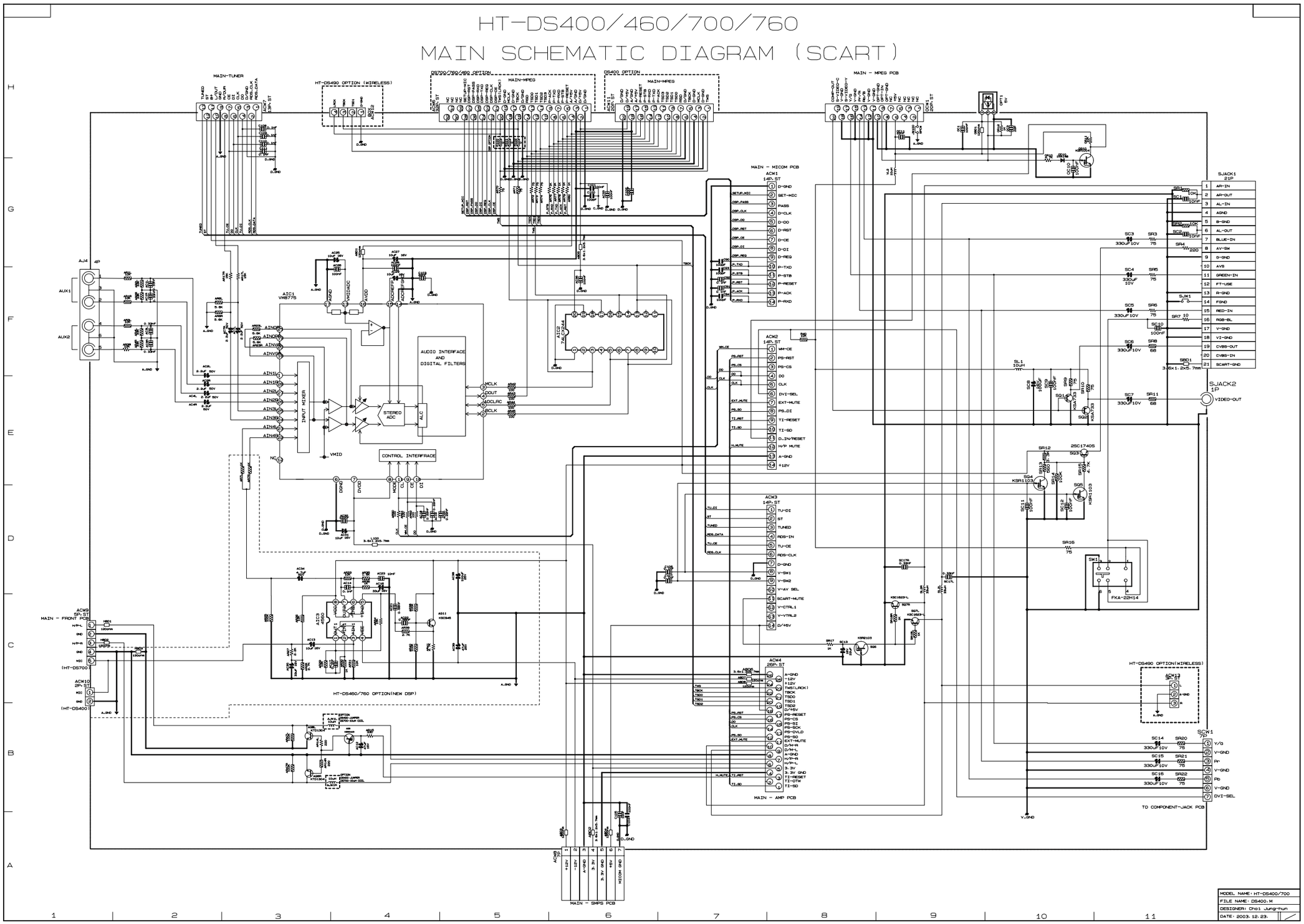
6-1-1 MAIN (RCA)


HT-DS400/460/700/760
MAIN SCHEMATIC DIAGRAM
MAIN-TUNER
HT-DS490 OPTION(WIRELESS)
MAIN-MPEG
MAIN - MPEG PCB
MAIN - MICOM PCB
MAIN - FRONT PCB
MAIN - AMP PCB
MAIN - SMPS PCB
AIC1
VM8775
AUDIO INTERFACE AND DIGITAL FILTERS
STEREO ADC
ALC
INPUT MIXER
CONTROL INTERFRACE
VIC1 BA7612F
VIC2 BA7612F
VIC3 BA7612F
VIC4 BA7612F
(OPTION)
HT-DB460/760 OPTION(NEW DSP)
OPTION DS400:JUMPER DS700:10uH COIL
OPTION T1-RESET DS700:10uH COIL
(HT-DS700)
(HT-DS400)
MODEL NAME: HT-DS400/700
FILE NAME: DS400.M
DESIGNER: Hee-Cheol Baek
DATE: 2003.12.01

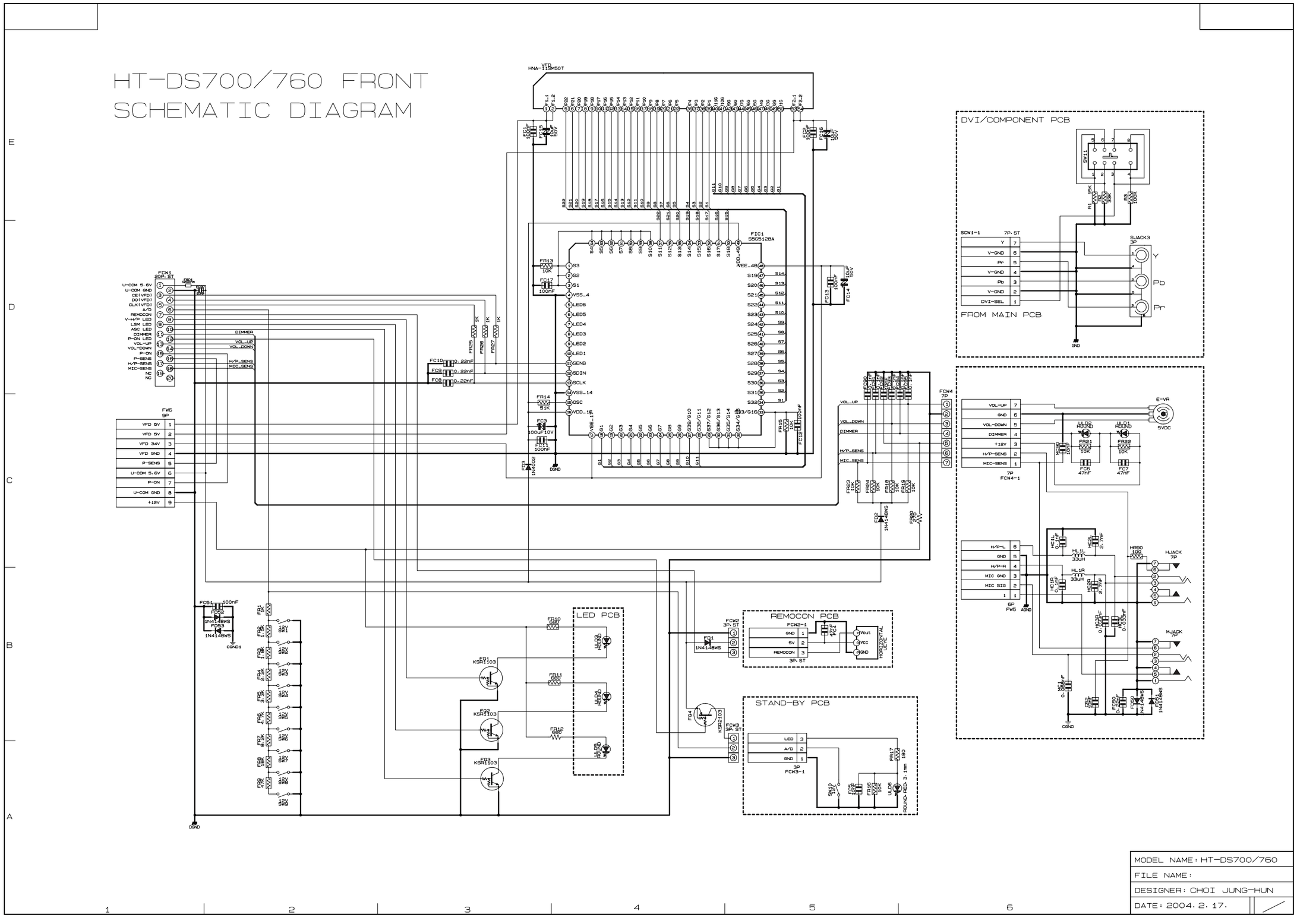
- This Document can not be used without Samsung's authorization -

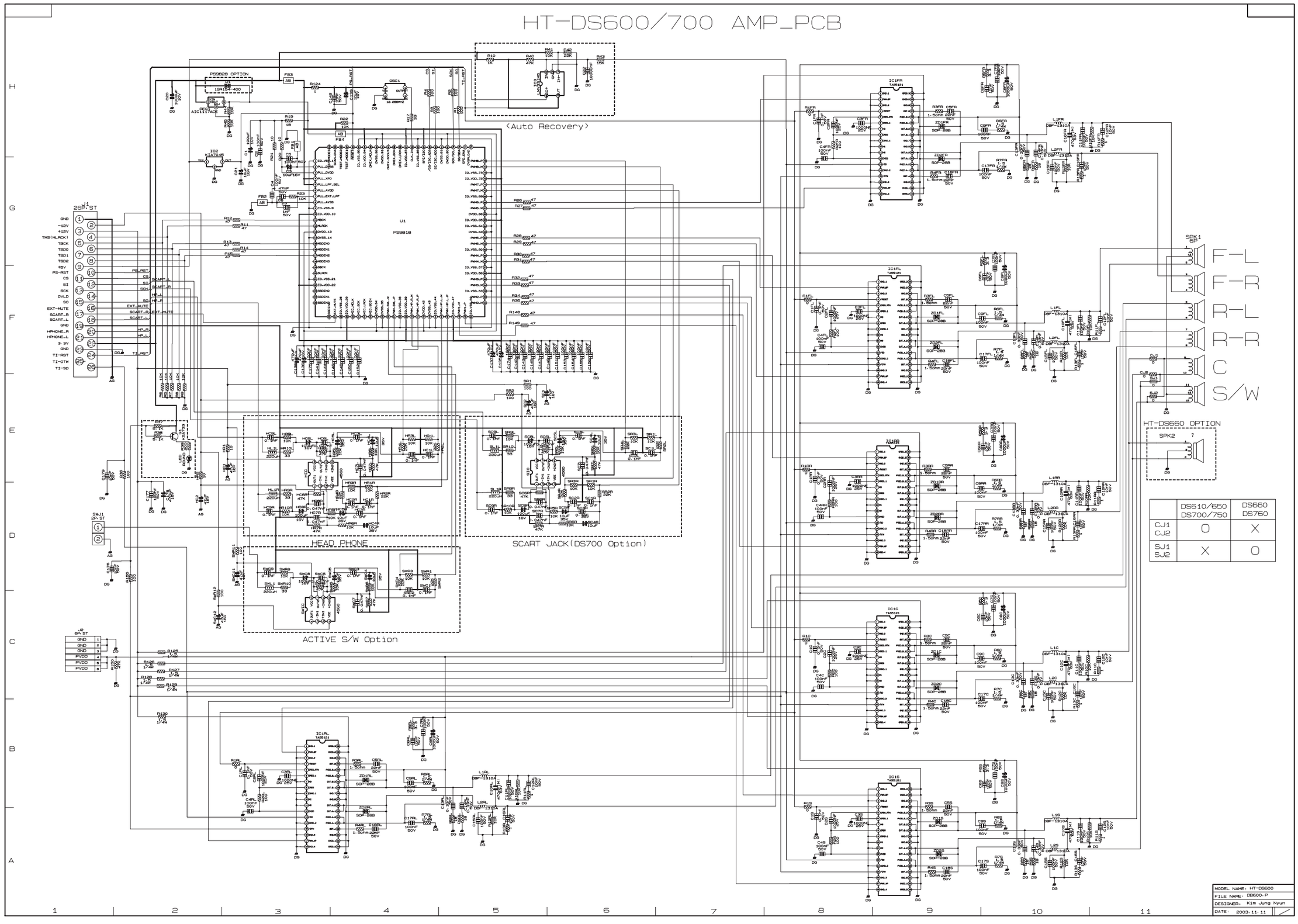
HT-DS400/460/700/760

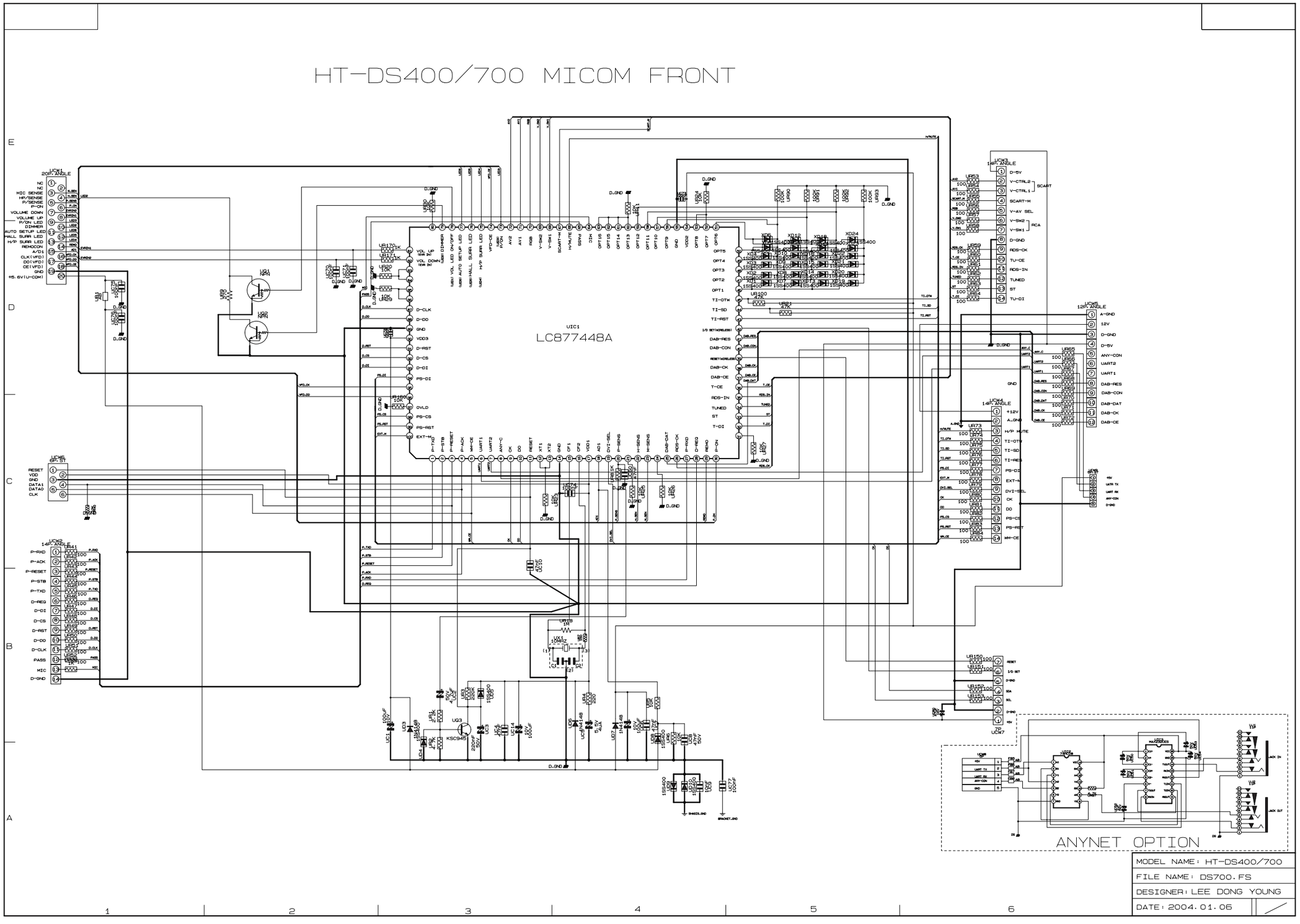
MAIN SCHEMATIC DIAGRAM (SCART)

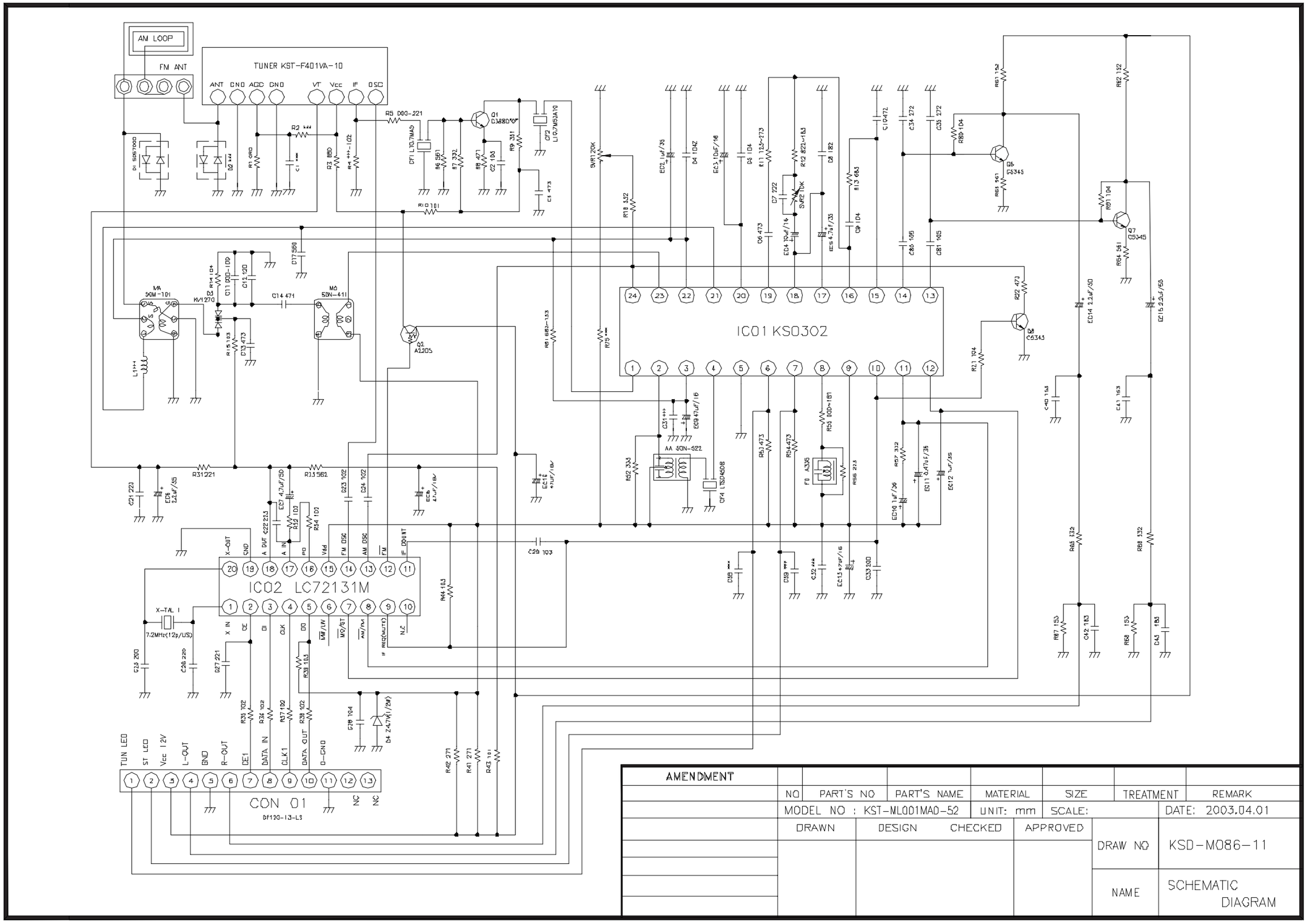


HT-DS700/760 FRONT SCHEMATIC DIAGRAM



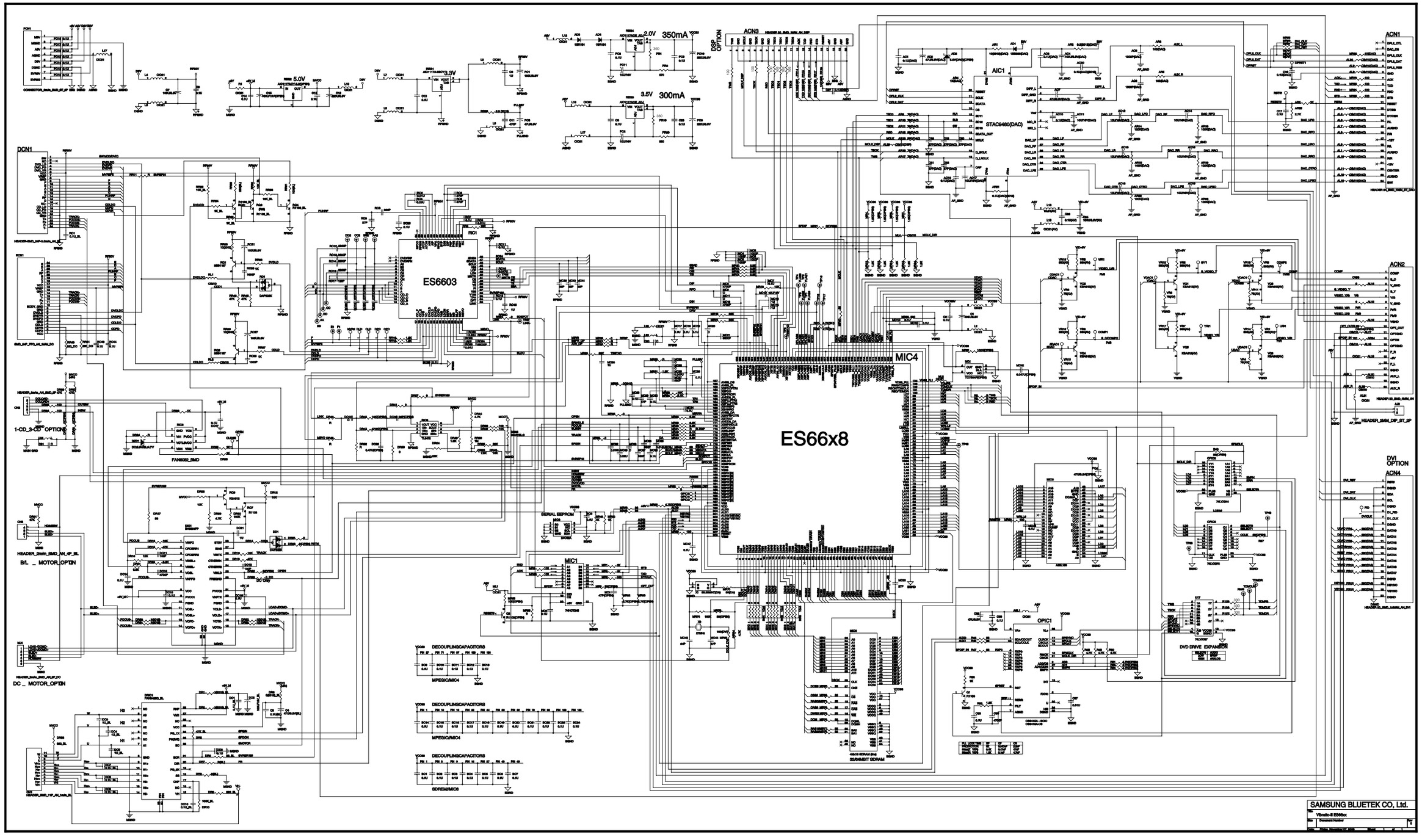

HT-DS600/700 AMP_PCB
<Auto Recovery>
HEAD PHONE
SCART JACK(DS700 Option)
ACTIVE S/W Option
HT-DS660 OPTION
SPK1
6P
F-L
F-R
R-L
R-R
C
S/W
SPK2
DS610/650 DS700/750
DS660 DS760
CJ1 CJ2
SJ1 SJ2
MODEL NAME: HT-DS600
FILE NAME: DB600.P
DESIGNER: Kim Jung Nyun
DATE: 2003.11.11


HT-DS400/700 MICOM FRONT
UIC1
LC877448A
ANYNET OPTION
MODEL NAME: HT-DS400/700
FILE NAME: DS700.FS
DESIGNER: LEE DONG YOUNG
DATE: 2004.01.06



AMENDMENT	NO	PART'S NO	PART'S NAME	MATERIAL	SIZE	TREATMENT	REMARK
	MODEL NO : KST-ML001MA0-52			UNIT: mm	SCALE:		DATE: 2003.04.01
	DRAWN	DESIGN	CHECKED	APPROVED	DRAW NO	KSD-M086-11	
					NAME	SCHEMATIC DIAGRAM	

*Note. The Component values of this Schematic diagram are subject to change without notice for improvement.


ES66x8
ES6603
STAC9460(DAC)
AIC1
MIC4
MIC1
OPIC1
ACN1
ACN2
ACN3
ACN4
DCN1
DVI OPTION
DSP OPTION
1-CD_3-CD OPTION
B/L _ MOTOR_OPTION
DC _ MOTOR_OPTION
DVD DRIVE EXPANDER
SERIAL EEPROM
MPEGIC/MIC4
MPEGIC/MIC6
SDRIEM/MIC6
SAMSUNG BLUETEK CO, Ltd.